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DISPLAY DRIVER ARCHITECTURE FOR A LIQUID CRYSTAL DISPLAY AND METHOD THEREFORE

ABSTRACT OF THE INVENTION

Methods and apparatus are provided for driving a liquid crystal microdisplay. The apparatus comprises more than one display driver integrated circuit where a liquid crystal microdisplay receives video information from at least two separate and distinct display driver integrated circuits. One of the display driver integrated circuits is designated as producing a master clock. The internal clocks of the other display driver integrated circuits are compared to the master clock and adjusted to reduce the phase difference to a delay that does not produce visible artifacts on the microdisplay. The frame polarity is compared to be the same from each integrated circuit driving the microdisplay before outputting video information. A frame synchronization signal is also generated from each display driver integrated circuit to ensure that they are all ready to provide information to the microdisplay.